



PATENT APPLICATION
ATTORNEY DOCKET NO. Q56320

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

RECEIVED
FEB 27 2002
TECHNOLOGY CENTER 2800

In re application of

Kaoru NARITA

Appln. No. 09/421,273

Group Art Unit: 2823

Confirmation No.: Not Yet Assigned

Examiner: J. Garcia

Filed: October 20, 1999

For: SEMICONDUCTOR DEVICE HAVING PROTECTION CIRCUIT IMPLEMENTED BY
BIPOLAR TRANSISTOR FOR DISCHARGING STATIC CHARGE CURRENT AND
PROCESS OF FABRICATION

EXCESS CLAIM FEE PAYMENT LETTER

Commissioner for Patents
Washington, D.C. 20231

Sir:

An Amendment Under 37 C.F.R. § 1.111 is attached hereto for concurrent filing in the
above-identified application. The resulting excess claim fee has been calculated as shown
below:

	After Amendment		Highest No. Previously Paid For					
All Claims	30	-	20	=	10	X	\$18.00	= \$180.00
Independent	3	-	3	=		X	\$84.00	= \$.00
					TOTAL			= \$180.00

EXCESS CLAIMS FEE PAYMENT LETTER
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A check for the statutory fee of \$180.00 is attached. Please charge any additional fee or credit any overpayment to our Deposit Account No. 19-4880. A duplicate copy of this letter is enclosed.

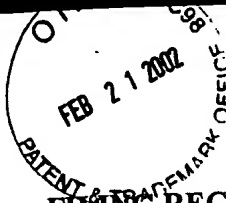
Respectfully submitted,



Paul J. Wilson
Registration No. 45,879

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Washington, D.C. 20037-3213
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Date: February 21, 2002



102

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PLEASE DATE STAMP AND RETURN TO US - BOX 235X

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PAPER(S) FILED ENTITLED:

1. SUBMISSION OF FORMAL DRAWINGS WITH ELEVEN (11)
SHEETS OF FORMAL DRAWINGS

SUGHRUE MION, PLLC
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ATTORNEY/SEC: HLB:PJW:db

Date Filed: November 30, 2001

